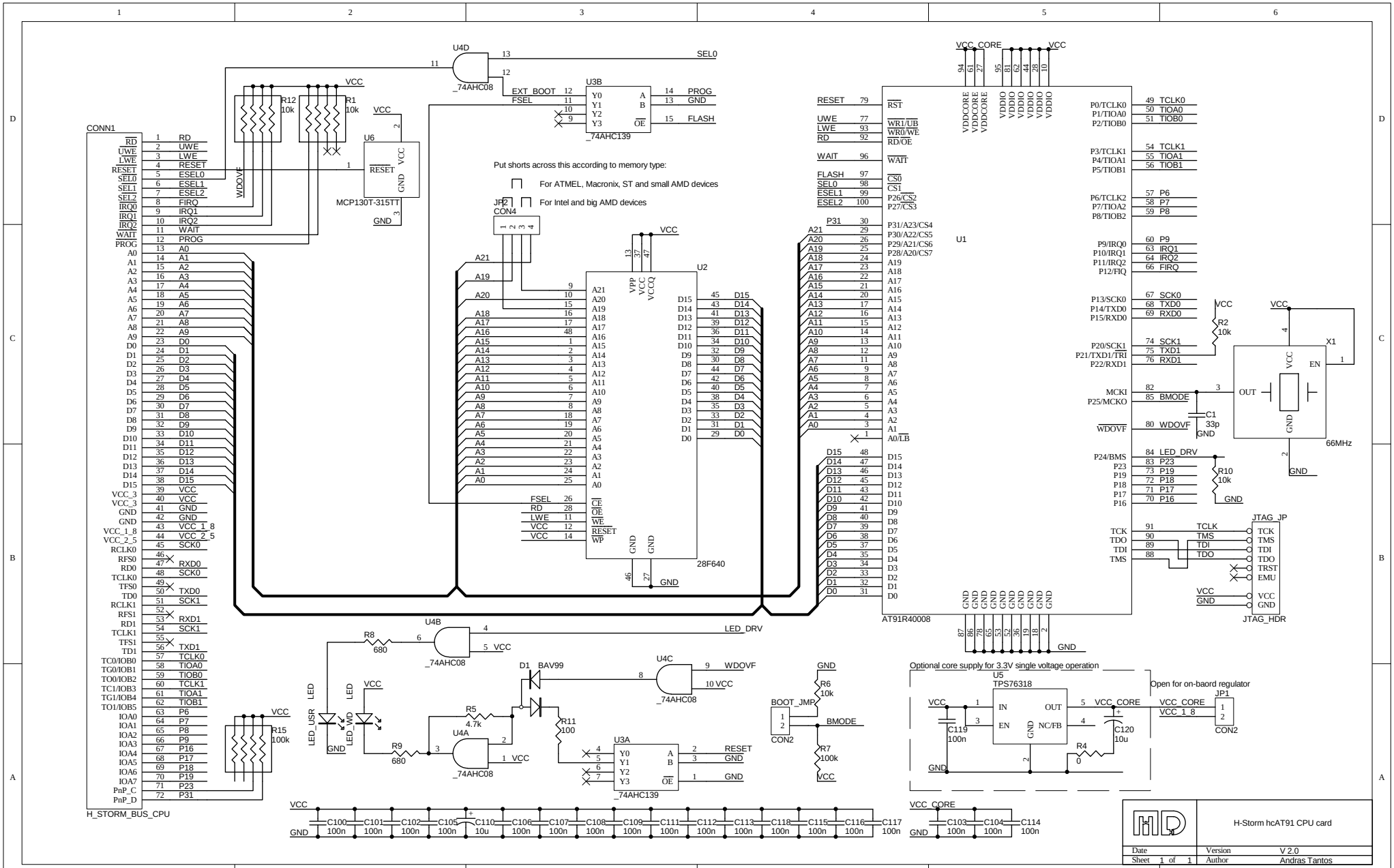
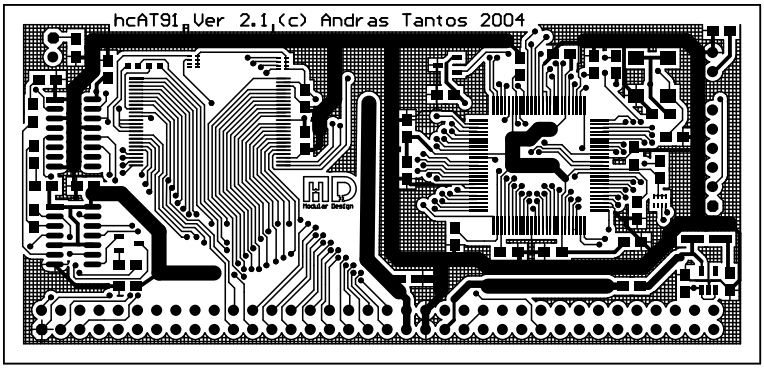
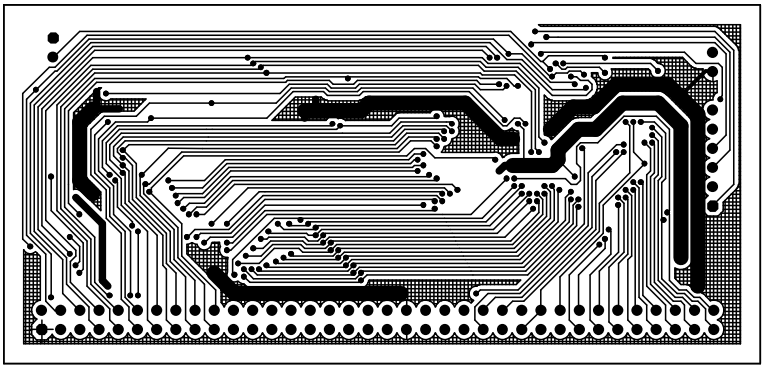
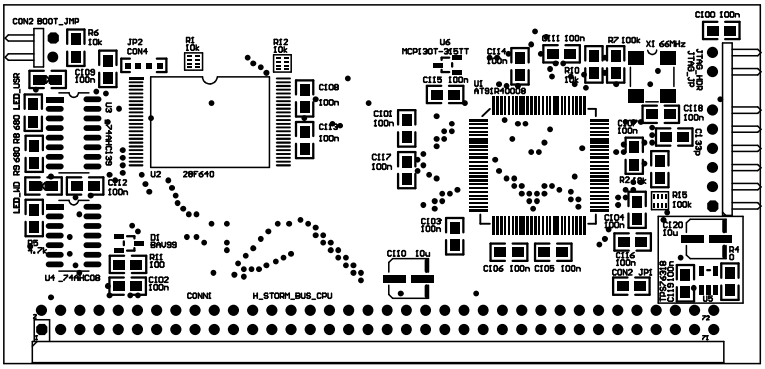

H-Storm hcAT91 CPU card
Date
Version V 2.0
Sheet 1 of 1
Author Andras Tantos
CONN1
H_STORM_BUS_CPU
U1
AT91R40008
U2
28F640
U3A
U3B
U4A
U4B
U4C
U4D
U5
TPS76318
U6
MCP130T-315TT
JTAG_HDR
JTAG_JP
X1
66MHz
JP1
JP2
CON2
CON4
R1 10k
R2 10k
R4 0
R5 4.7k
R6 10k
R7 100k
R8 680
R9 680
R10 10k
R11 100
R12 10k
R15 100k
C1 33p
C119 100n
C120 10u
D1 BAV99
LED_USR
LED_WD
BOOT_JMP
BMODE
VCC_CORE
Put shorts across this according to memory type:
For ATMEL, Macronix, ST and small AMD devices
For Intel and big AMD devices
Optional core supply for 3.3V single voltage operation
Open for on-baord regulator


hcAT91 Ver 2.1 (c) Andras Tantos 2004




CON2 BOOT_JMP
R6 10k
JP2 CON4
R1 10k
R12 10k
C100 100n
U6 MCP130T-3083T
C114 100n
C111 100n
R7 100k
X1 66MHz
C115 100n
U1 AT91RM9200
R10 10k
C108 100n
C109 100n
C110 100n
C101 100n
C117 100n
C107 100n
C118 100n
C119
R24 0k
R15 100k
C103 100n
C104 100n
C120 10u
R4 0
C106 100n
C105 100n
C116 100n
CON2 JP1
U5 AT25DF161
U3 74HC139
U2 28F640
C112 100n
D1 BAV99
R11 100
C102 100n
U4 74HC08
C110 10u
CONN1
H_STORM_BUS_CPU
LED_JP1 R9 680 R8 680 LED_USR